QUICKSWITCH[®] PRODUCTS HIGH-SPEED CMOS QUICKSWITCH 32-BIT MULTIWIDTH[™] BUS SWITCHES

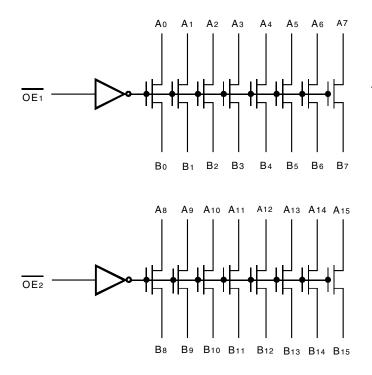
FEATURES:

- · Enhanced N channel FET with no inherent diode to Vcc
- · Bidirectional switches connect inputs to outputs
- · Zero propagation delay, zero ground bounce
- Internal 25Ω resistors for low noise
- · Flow-through pinout for easy layout
- · Undershoot clamp diodes on all switch and control inputs
- TTL-compatible control inputs
- Available in 80-pin MilliPaQ[™] package

APPLICATIONS:

- · Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Bus switching and isolation
- Power conservation
- · Logic replacement (data processing)
- Capacitance isolation
- Clock gating

FUNCTIONAL BLOCK DIAGRAM



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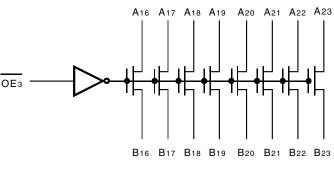
INDUSTRIAL TEMPERATURE RANGE

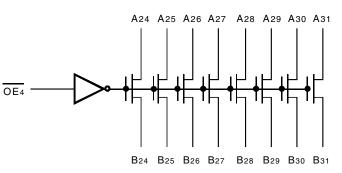
DESCRIPTION:

The QS34X2245 is a member of the MultiWidthTM family of QuickSwitch devices and provides a set of 32 high-speed CMOS compatible bus switches in a flow-through pinout. This device is available in the MillipaQ package, the worlds first small outline 32-bit solution. The low ON-resistance of the QS34X2245 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. Internal 25 Ω resistors reduce reflection noise in high-speed applications. When Output Enable (OEn) is low, the switches are turned on, connecting bus A to bus B. When OEn is high, the switches are turned off. This device is ideally suited for 32/64 bit applications where board space is at a premium.

QuickSwitch devices provide speeds an order of magnitude faster than conventional logic devices.

The QS34X2245 is characterized for operation at -40°C to +85°C.





OCTOBER 2008

PINCONFIGURATION

				1
№ [1	•	80	
A0 🗌	2		79	
A1 🗌	3		78	В0
A2 🗌	4		77	B1
Аз 🗌	5		76	B2
A4 🗌	6		75	Вз
A5 🗌	7		74	B 4
A6 🗌	8		73	_ B5
A7 🗌	9		72	B 6
GND	10		71	В7
№ [11		70	
A8 [12		69	OE2
A9 🗌	13		68	B 8
A10	14		67	B9
A11	15		66	B10
A12	16		65	B11
A13	17		64	B12
A14	18		63	B13
A15	19		62	B14
GND	20		61	B15
NC	21		60	
A16	22		59	OE3
A17	23		58	B16
A18	24		57	B17
A19	25		56	B18
A20	26		55	B19
A21	27		54	B20
A22	28		53	B21
A23	29		52	B22
GND	30		51	B23
NC	31		50	
A24	32		49	OE4
A25	33		48	B24
A26	34		47	B25
A27	35		46	B26
A28	36		45	B27
A29	37		44	B28
A30	38		43	B29
A31	39		42	B 30
GND	40		41	B31
	L	MILLIPAQ TOP VIEW		J

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit
VTERM ⁽²⁾	Supply Voltage to Ground	-0.5 to +7	V
VTERM ⁽³⁾	DC Switch Voltage Vs	–0.5 to +7	V
VTERM ⁽³⁾	DC Input Voltage VIN	–0.5 to +7	V
VAC	AC Input Voltage (pulse width \leq 20ns)	-3	V
Ιουτ	DC Output Current	120	mA
Рмах	Maximum Power Dissipation (TA =70°C)	1.4	W
Tstg	Storage Temperature	-65 to +150	°C

NOTE:

 Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

2. Vcc terminals.

3. All terminals except Vcc.

CAPACITANCE

 $(TA = +25^{\circ}C, f = 1.0MHz, VIN = 0V, VOUT = 0V)$

Pins	Тур.	Max. ⁽¹⁾	Unit
Control Pins	3	4	pF
Quickswitch Channels (Switch OFF)	7	8	рF

NOTE:

1. This parameter is measured at characterization but not tested.

PINDESCRIPTION

Pin Names	Description
OEn	Output Enable
An	Data I/Os
Bn	Data I/Os

FUNCTION TABLE⁽¹⁾

ŌĒn	Outputs
Н	Disconnected
L	An = Bn

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

 ${\it Following \, Conditions \, Apply \, Unless \, Otherwise \, Specified:}$

Industrial: TA = -40° C to $+85^{\circ}$ C, VCC = 5.0V $\pm 5\%$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
Vih	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	_	_	V
Vil	Input LOW Level	Guaranteed Logic LOW for Control Pins	_	_	0.8	V
lin	Input LeakageCurrent (Control Inputs) ⁽²⁾	$0V \le VIN \le VCC$	_	—	±1	μA
loz	Off-State Output Current (Hi-Z)	$0V \le VOUT \le VCC$, Switches OFF	_	_	±1	μA
Ron ⁽³⁾	Switch ON Resistance	VCC = Min., $VIN = 0V$, $ION = 30mA$	18	23	35	Ω
		VCC = Min., $VIN = 2.4V$, $ION = 15mA$	18	25	40	
Vp	Pass Voltage ⁽²⁾	$V_{IN} = V_{CC} = 5V$, $I_{OUT} = -5\mu A$	3.7	4	4.2	V

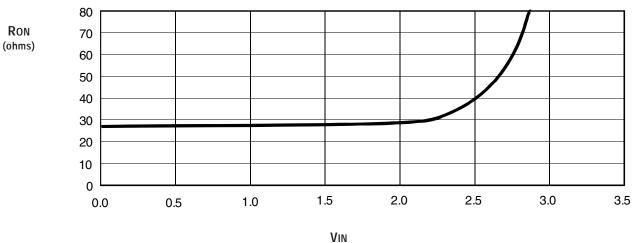
NOTES:

1. Typical values are at Vcc = 5.0V, TA = 25°C.

2. Pass Voltage is guaranteed but not production tested.

3. Rout changed on March 8, 2002. See rear page for more information.

TYPICAL ON RESISTANCE vs VIN AT Vcc = 5V



(Volts)

POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
lcco	Quiescent Power Supply Current	Vcc = Max., VIN = GND or Vcc, f = 0	12	μA
∆lcc	Power Supply Current per Control Input HIGH ⁽²⁾	Vcc = Max., VIN = 3.4V, f = 0	1.5	mA
ICCD	Dynamic Power Supply Current per MHz ⁽³⁾	Vcc = Max., A and B pins open Control Inputs Toggling at 50% Duty Cycle	0.25	mA/MHz

NOTES:

1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.

2. Per TLL driven input (VIN = 3.4V, control inputs only). A and B pins do not contribute to Δ Icc.

3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

 $T_A = -40^{\circ}C \text{ to } +85^{\circ}C, V_{CC} = 5.0V \pm 5\%;$

CLOAD = 50pF, RLOAD = 500Ω unless otherwise noted.

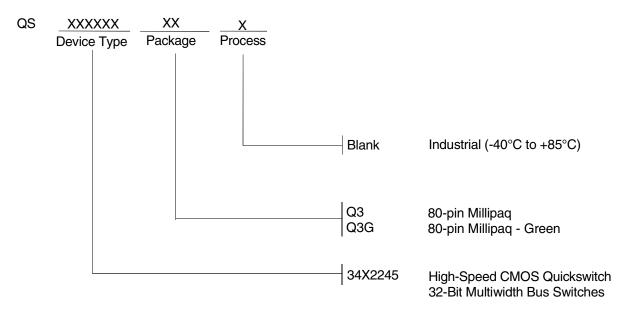
Symbol	Parameter	Min. ⁽¹⁾	Тур.	Max.	Unit
tPLH	Data Propagation Delay ^(1,2)	—	—	1.25	ns
t PHL	An to/from Bn				
tPZL	Switch Turn-on Delay	0.5	—	6.6	ns
tpzh	OE to An/Bn				
tPLZ	Switch Turn-off Delay ⁽¹⁾	0.5	—	5.2	ns
tphz	OE to An/Bn				

NOTES:

1. Minimums are guaranteed but not production tested.

2. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 1.25ns for CL = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION



As per PCN L0201-02, the Output Resistance (Row) specifications have changed as of March 8, 2002. The original specifications were:

Parameter	Description	Min.	Тур.	Max.	Unit
Ron	VCC = Min, VIN = 0V, ION = 30mA	20	28	40	Ω
	VCC = Min, VIN = 2.4V, ION = 15mA	20	35	48	



CORPORATE HEADQUARTERS 6024 Silver Creek Valley Road San Jose, CA 95138

for SALES: 800-345-7015 or 408-284-8200 fax: 408-284-2775 www.idt.com

for Tech Support: logichelp@idt.com